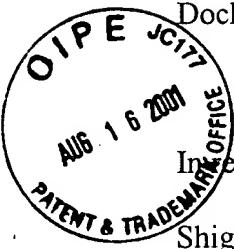


Receipt

#5 P. Br

PATENT

10/23/01



Docket No.: 57454-072

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Shigeki OHBAYASHI

Serial No.: 09/829,046

Group Art Unit: 2818

Filed: April 10, 2001

Examiner: not yet assign

For: STATIC SEMICONDUCTOR MEMORY DEVICE HAVING T-TYPE BIT LINE
STRUCTURE

RECEIVED
OCT 19 2001
TC 280 MAIL ROOM

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
Washington, DC 20231

Sir:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that the Title on the Official Filing Receipt is incorrect. Attached is a copy of the Declaration and Power of Attorney, which evidences that the Title should read as:

STATIC SEMICONDUCTOR MEMORY DEVICE HAVING T-TYPE BIT LINE
STRUCTURE.

It is requested that a corrected filing receipt be issued.

Respectfully submitted,
MCDERMOTT, WILL & EMERY

Stephen A. Becker
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Date: August 16, 2001
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UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS
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WASHINGTON, D.C. 20231
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| APPLICATION NUMBER | FILING DATE | GRP ART UNIT | FIL FEE REC'D | ATTY.DOCKET.NO | DRAWINGS | TOT CLAIMS | IND CLAIMS |
|--------------------|-------------|--------------|---------------|----------------|----------|------------|------------|
| 09/829,046 | 04/10/2001 | 2818 | 710 ✓ | 57454-072 | 14 ✓ | 7 ✓ | 1 ✓ |

O I P E
AUG 16 2001
SC 177
PATENT & TRADEMARK OFFICE
MADDERMOTT, WILL & EMERY
600 13th Street, N.W.
Washington, DC 20005-3096

CONFIRMATION NO. 8290

FILING RECEIPT



OC000000006073792

Date Mailed: 05/14/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Shigeki Ohbayashi, Hyogo, JAPAN;

Assignment For Published Patent Application

Mitsubishi Denki Kabushiki Kaisha;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2000-383164(P) 12/18/2000

If Required, Foreign Filing License Granted 05/14/2001

Projected Publication Date: 06/20/2002

Non-Publication Request: No

Early Publication Request: No

Title

Static semiconductor memory device having T-type bit line

Structure

RECEIVED
OCT 19 2001
1C 2800 MAIL ROOM

Preliminary Class

365

Data entry by : TADESSE, ETAGEAN

Team : OIPE

Date: 05/14/2001



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